

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. Currently Amended) A camera comprising:

a mode selector for selecting one of a first mode for executing [[a]] first image data processing to an image data taken by an image pickup device and a second mode for executing [[a]] second image data processing ~~contents of which are different from that of~~ the first image data processing;

an electronic circuit arrangement in which a logic circuit is obtained when a predetermined program is written, said logic circuit is configured depending on the written program, and said logic circuit executes a predetermined operation on inputted image data;

~~a calculator in which a logic circuit for executing a predetermined operation to an inputted image data when a predetermined program is written;~~

a memory for memorizing a first program corresponding to the first image data processing and a second program corresponding to the second image data processing; and

a controller for reading the first program from the memory and writing it in the ~~calculator~~ electronic circuit arrangement when the first mode is selected by the mode selector and for reading the second program from the memory and writing it in the ~~calculator~~ electronic circuit arrangement when the second mode is selected by the mode selector.

2. (Original) The camera in accordance with claim 1, wherein

the first mode is an image pickup mode for taking an image data by photoelectric transferring an optical image of an object;

the second mode is a reproducing mode for reproducing an image on a display by using an image data taken by the image pickup mode;

the first image data processing is a data compression processing of the image data taken by the image pickup mode; and

the second image data processing is a data extension processing of a compressed image data.

3. (Currently Amended) The camera in accordance with claim 1, wherein the ~~calculator~~ electronic circuit arrangement is a field programmable gate array.

4. (Currently Amended) A camera comprising:

a connection portion to which a first equipment and a second equipment can alternatively be connected, the first equipment ~~[[be]]~~ being communicative ~~[[a data]]~~ with the camera by a first data communication standard, and the second equipment ~~[[be]]~~ being communicative ~~[[a data]]~~ with the camera by a second data communication standard;

a detector for judging a kind of data communication standard of an equipment connected to the connection portion;

an electronic circuit arrangement in which a logic circuit is obtained when a predetermined program is written, said logic circuit is configured depending on the written program, and said logic circuit executes a predetermined operation on inputted image data;

~~a calculator in which a logic circuit for executing a predetermined operation to an inputted image data when a predetermined program is written;~~

a memory for memorizing a first program corresponding to a first image data communication processing fitting for the first data communication standard and a second program corresponding to a second image data communication processing fitting for the second data communication standard; and

a controller for reading the first program from the memory and writing it in the ~~calculator~~ electronic circuit arrangement when the kind of the data communication standard of the equipment connected to the communication portion is judged as the first data communication standard by the detector and for reading the second program from the memory and writing it in the ~~calculator~~ electronic circuit arrangement when the kind of the

data communication standard of the equipment is judged as the second data communication standard.

5. (Original) The camera in accordance with claim 4, wherein the first data communication standard and the second data communication standard are respectively a USB standard and an RS-232C standard.

6. (Original) The camera in accordance with claim 4, wherein the equipment to be connected to the connection portion is an equipment which can execute an image data processing.

7. (Currently Amended) The camera in accordance with claim 4, wherein the ~~calculator~~ electronic circuit arrangement is a field programmable gate array.

8. (Currently Amended) A camera comprising:  
an image processing selector for selecting an image processing among a plurality of image processing corresponding to different characteristics with respect to quality of an image;

an electronic circuit arrangement in which a logic circuit is obtained when a predetermined program is written, said logic circuit is configured depending on the written program, and said logic circuit executes a predetermined operation on image data input thereto;

~~a calculator in which a logic circuit for executing a predetermined operation to an inputted image data when a predetermined program is written;~~

a memory for memorizing a plurality of programs corresponding to the plurality of image processing; and

a controller for reading a program corresponding to the image processing selected by the image processing selector and writing it in the ~~calculator~~ electronic circuit arrangement.

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9. (Original) The camera in accordance with claim 8, wherein the image processing with respect to the quality of the image is a gamma compensation.

10. (Original) The camera in accordance with claim 8, wherein the image processing with respect to the quality of the image is a contour emphasizing or unemphasizing compensation of the image.

11. (Currently Amended) The camera in accordance with claim 8, wherein the ~~calculator~~ electronic circuit arrangement is a field programmable gate array.